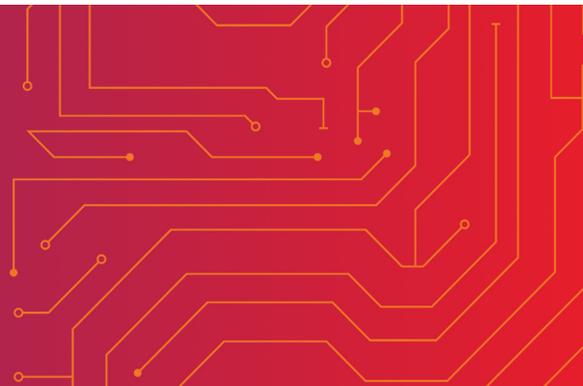# Sigasi;

redefine.digital.design

Deal with the complexity of VHDL, Verilog and SystemVerilog

Sigasi Studio is an intelligent hardware design tool that features advanced programming assistance for engineering teams who create microchips for strictly regulated and safety sensitive environments.

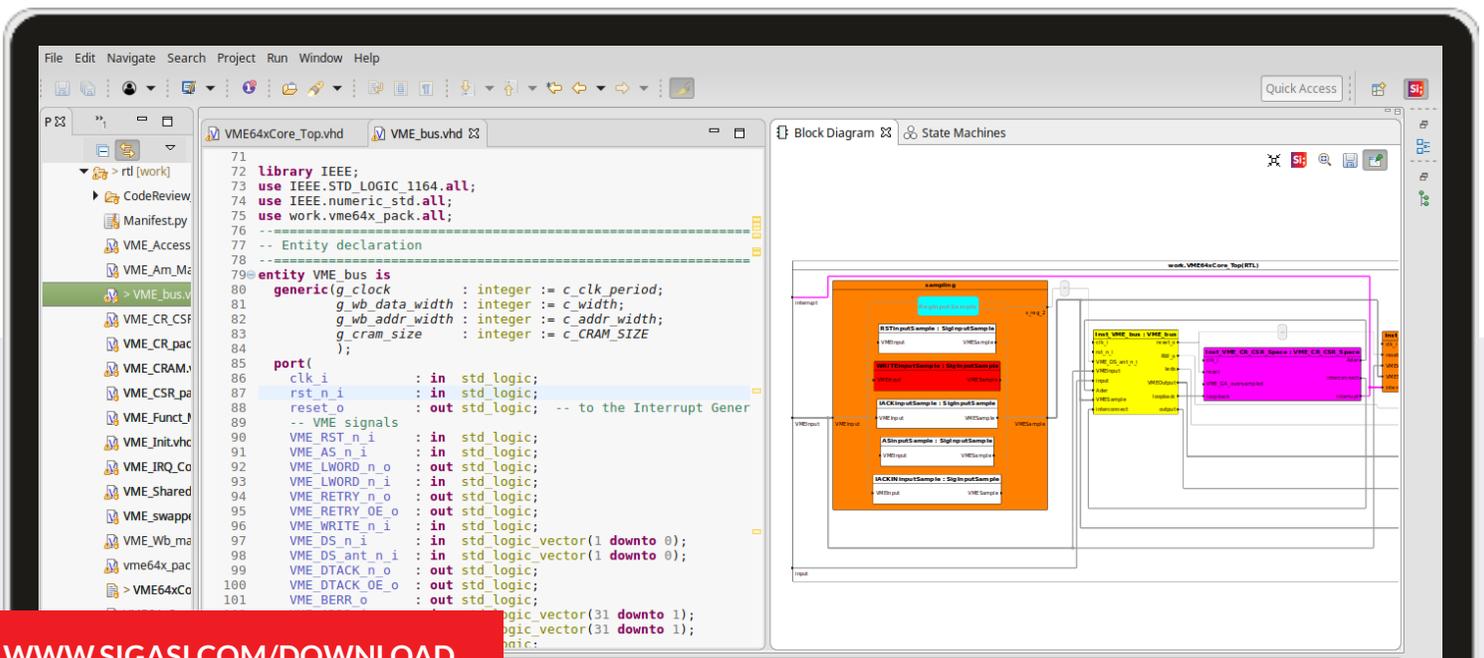## HARDWARE DESIGN MADE FASTER, EASIER AND MORE EFFICIENT

HDL (VHDL/Verilog/SystemVerilog) design is difficult and complex. That's why we created Sigasi Studio as a design creation tool that deals with that complexity. Sigasi Studio makes your hardware design:

Faster_ Our secret ingredient is the super fast built-in compiler. Because Sigasi Studio understands your code while you type, it can help you be more productive, produce higher quality work and excell in your work.

Easier_ A basic text editor just won't do if you want to write code like a pro. Sigasi Studio is an intelligent design tool that offers advanced design assistance. Why walk if you can drive… or get driven to your destination?

More efficient_ Sigasi Studio guides you through complex code designs. With instant feedback on errors and auto-completion suggestions. Reducing development time and helping you and your team write better code.

SAVE TIME AND MONEY

Sigasi Studio is like your personal assistant. It provides you with:

Code editing_ Code completion, including component instantiations, based on where you are in the code. Sigasi Studio marks your syntax errors as you type, so you can fix them right away. The code is formatted and beautified consistently.

Code browsing_ Sigasi Studio serves as a code browser, so that you can navigate through your designs to understand large and complex legacy designs. With graphical browsing you can create visuals of your code, updated instantly and cross-linked to your code.

Code checking_ See errors while you type and get warnings about dubious code. Save time and money as you need less time to write better code and you are able to free up valuable time at code reviews.

Documentation_ Generate complete state machine diagrams or block diagrams that are always up-to-date. Text for documentation can be written as inline comments.

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FREE TRIAL download a copy and try Sigasi Studio today

Sigasi;

FEATURES MATRIX

	SIGASI STUDIO CREATOR — Powerful design entry tool and code browser for all your VHDL or SystemVerilog projects	SIGASI STUDIO XL — Our trusted solution for mixed-language projects with powerful extra features	SIGASI STUDIO XPRT — Our next-generation flagship product with powerful visual feed-back tools and documentation
Great code editor	✓	✓	✓
Autocomplete	✓	✓	✓
Syntax validation & Quick Fixes	✓	✓	✓
Format code	✓	✓	✓
Rename refactoring	✓	✓	✓
Find references	✓	✓	✓
Integrates with simulators	✓	✓	✓
Mixed Language (VHDL/SystemVerilog)		✓	✓
Supports UVM		✓	✓
Hierarchy View		✓	✓
Offline updates		✓	✓
Integrates with linters		✓	✓
Code formatting options		✓	✓
Net search		✓	✓
Advanced Type Time Linting		✓	✓
VUnit integration			✓
Graphical views			
Compilation Dependencies		✓	✓
Block Diagram			✓
State Machine Diagram			✓
Documentation Generation			
Generate HTML and PDF Documentation			✓
Documentation Source Available			✓

GRAPHICAL VIEWS	LIVE UPDATE	NAVIGATION	EXPORT	CONFIGURATION
State Machine Diagram	Type Time	Linked with source	PNG/SVG	Graphics config file
Block Diagram	Save Time	Linked with source	PNG/SVG	Graphics config file
Dependencies	Save Time	Linked with source	PNG/SVG	File or Project level

DOCUMENTATION GENERATION

One click HTML and PDF export	Live preview
Doc sources for custom documentation generation	Includes Block & FSM diagrams