

Deal with the complexity of VHDL and SystemVerilog

Sigasi Studio is an intelligent hardware design tool that features advanced programming assistance for engineering teams who create microchips for strictly regulated and safety sensitive environments.

HARDWARE DESIGN MADE EASIER, MORE EFFICIENT, MORE FUN

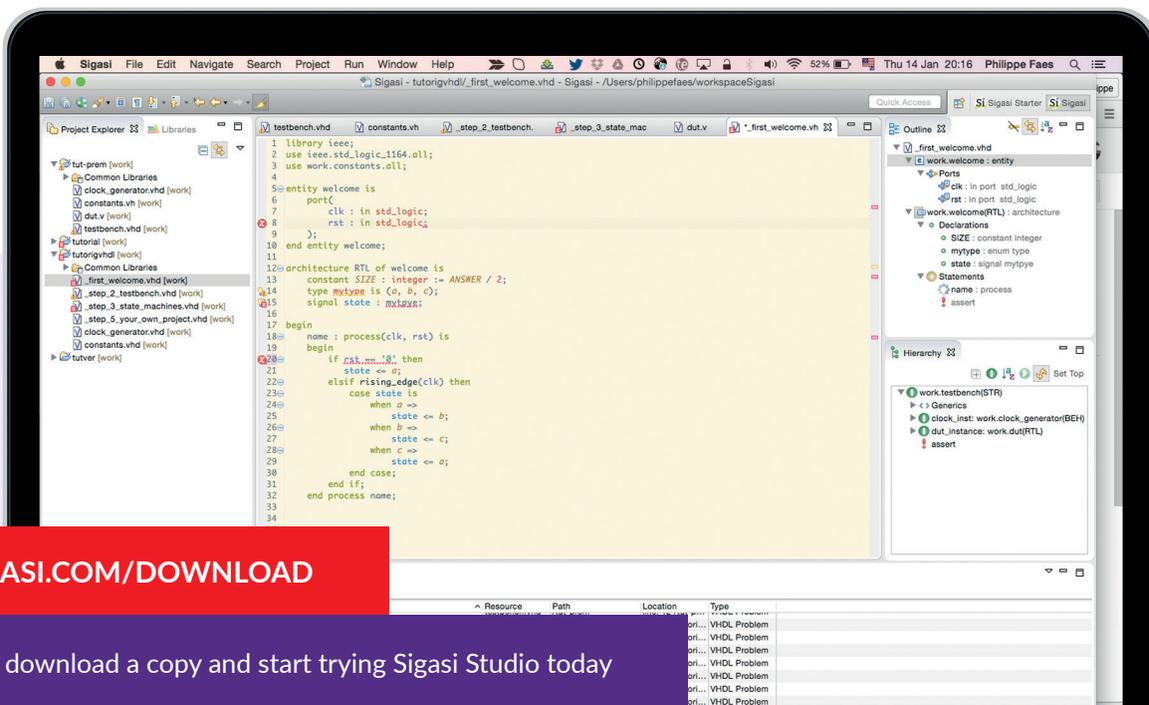
HDL (VHDL/SystemVerilog) design is difficult and complex. That's why we created Sigasi Studio as a design creation tool that deals with that complexity. Sigasi Studio makes your hardware design:

-  **Easier_** A basic text editor just won't do if you want to write code like a pro. Sigasi Studio is an intelligent design tool that offers advanced design assistance. Why walk if you can drive... or get driven to your destination?
-  **More efficient_** Sigasi Studio guides you through complex code designs. With instant feedback on errors and auto-completion suggestions. Reducing development time and helping you and your team write better code.
-  **More fun_** Our secret ingredient is the super fast built-in compiler. Because Sigasi Studio understands your code while you type, it can help you be more productive, produce higher quality work and have fun in the process.

SAVE TIME AND MONEY

Sigasi Studio is like your personal assistant. It provides you with:

-  **Code editing_** Code completion, based on where you are in the code including component instantiation. Sigasi Studio marks your syntax errors as you type, so you can fix them right away. The code is formatted and beautified consistently.
-  **Code browsing_** Sigasi Studio serves as a code browser, so that you can navigate through your designs to understand large and complex legacy designs. With graphical browsing you can create visuals of your code, updated instantly and cross-linked to your code.
-  **Code checking_** See errors while you type and get warnings about dubious code. Save time and money as you need less time to write better code and you are able to free up valuable time at code reviews.
-  **Documentation_** Generate complete state machine diagrams or block diagrams that are always up-to-date. Text for documentation can be written as inline comments.



WWW.SIGASI.COM/DOWNLOAD

FREE TRIAL download a copy and start trying Sigasi Studio today

FEATURES MATRIX

	SIGASI STUDIO STARTER	SIGASI STUDIO CREATOR	SIGASI STUDIO XL	SIGASI STUDIO XL DOC
	Great editor. Enter the world of professional design free of charge.	The go-to design entry tool and code browser for all your projects.	Our flagship product. All the features of Creator but even more powerful.	An effortless way to generate diagrams, and create a PDF with all the relevant info.
Great code editor	✓	✓	✓	✓
Autocomplete	✓	✓	✓	✓
Syntax validation	✓	✓	✓	✓
Format code	✓	✓	✓	✓
Projects		✓	✓	✓
Rename elements		✓	✓	✓
Find references		✓	✓	✓
Basic validation		✓	✓	✓
QuickFixes		✓	✓	✓
Integrates with simulator		✓	✓	✓
Mixed Language (VHDL/SystemVerilog)			✓	✓
Supports UVM			✓	✓
Chip hierarchy			✓	✓
View compilation dependencies			✓	✓
Offline updates			✓	✓
Integrates with linter			✓	✓
Code formatting options			✓	✓
Net search			✓*	✓*
Extra validation (advanced linting)			✓*	✓*
Block diagram				✓*
State machine diagram				✓*
Generate PDF documentation				✓*

1 **Code Lint_** Most of our customers use their own coding style guides for writing VHDL code. Team leaders often feel that in doing this, they are losing valuable time discussing trivial coding issues. Fortunately, Sigasi Studio offers your coding style rules as interactive linting rules. Sigasi Studio can flag coding style violations in real-time, much like built-in spell checkers. Fixing violations will only takes seconds.

* VHDL only

2 **XL Doc_** Stop wasting time on outdated documentation. With this add-on all it takes is one push of a button and you will generate complete state machine diagrams or block diagrams. Something that normally can take up to 4 hours per drawing. But we go even further: our Documentation Generator creates a PDF with all the relevant information from your project. Information will not be duplicated, so it is always consistent. Documentation is kept up-to-date automatically and text for documentation can be written as inline comments in the code.



SELECTED CUSTOMERS

THALES

NXP

DOLBY
DIGITAL

ABB

ASML

HARRIS



AIRBUS