




Deal with the complexity of VHDL and SystemVerilog

Sigasi Studio is an intelligent hardware design tool that features advanced programming assistance for engineering teams who create microchips for strictly regulated and safety sensitive environments.





HARDWARE DESIGN MADE EASIER, MORE EFFICIENT, MORE FUN

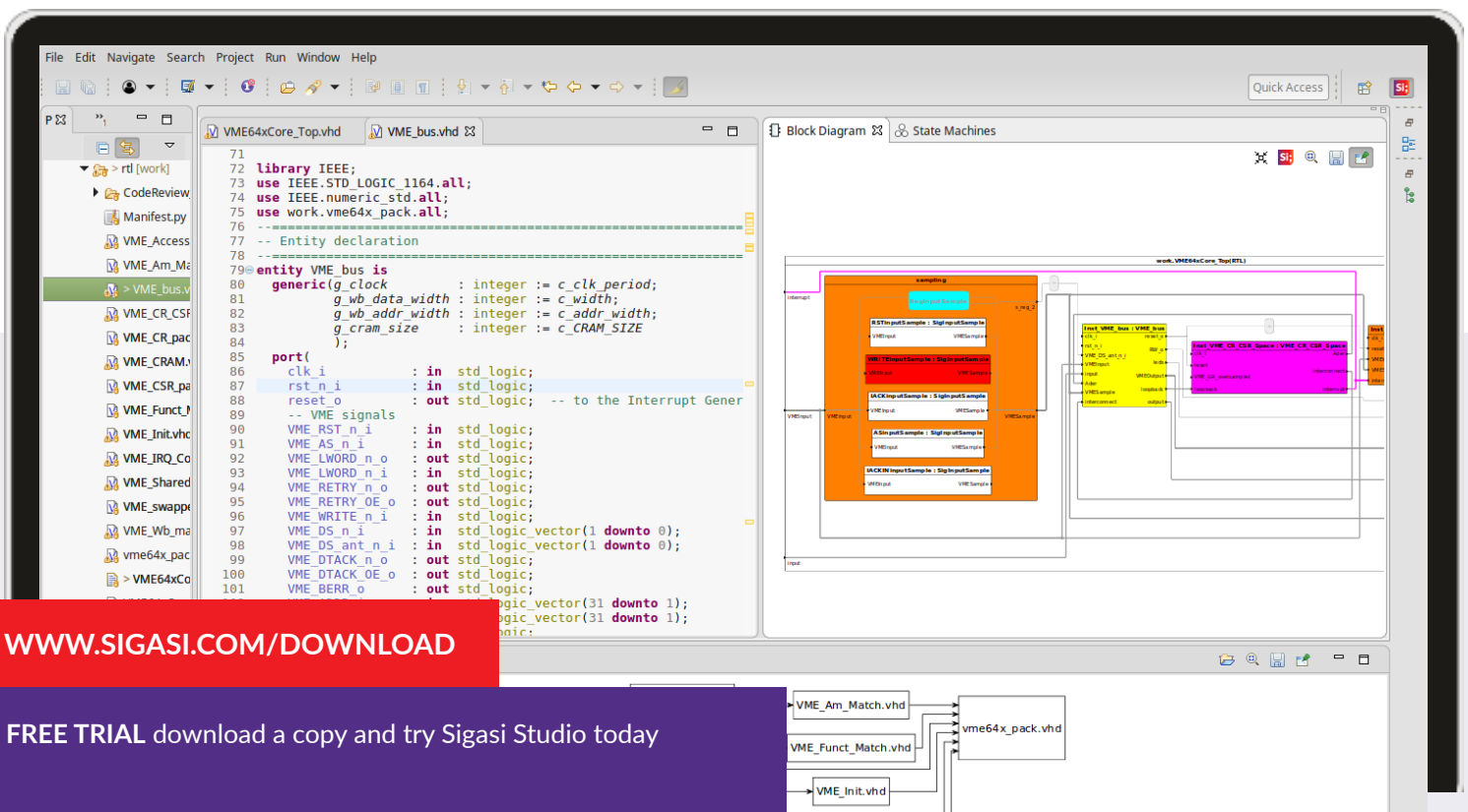
HDL (VHDL/SystemVerilog) design is difficult and complex. That's why we created Sigasi Studio as a design creation tool that deals with that complexity. Sigasi Studio makes your hardware design:

-  **Easier_** A basic text editor just won't do if you want to write code like a pro. Sigasi Studio is an intelligent design tool that offers advanced design assistance. Why walk if you can drive... or get driven to your destination?
-  **More efficient_** Sigasi Studio guides you through complex code designs. With instant feedback on errors and auto-completion suggestions. Reducing development time and helping you and your team write better code.
-  **More fun_** Our secret ingredient is the super fast built-in compiler. Because Sigasi Studio understands your code while you type, it can help you be more productive, produce higher quality work and have fun in the process.

SAVE TIME AND MONEY

Sigasi Studio is like your personal assistant. It provides you with:

-  **Code editing_** Code completion, including component instantiations, based on where you are in the code. Sigasi Studio marks your syntax errors as you type, so you can fix them right away. The code is formatted and beautified consistently.
-  **Code browsing_** Sigasi Studio serves as a code browser, so that you can navigate through your designs to understand large and complex legacy designs. With graphical browsing you can create visuals of your code, updated instantly and cross-linked to your code.
-  **Code checking_** See errors while you type and get warnings about dubious code. Save time and money as you need less time to write better code and you are able to free up valuable time at code reviews.
-  **Documentation_** Generate complete state machine diagrams or block diagrams that are always up-to-date. Text for documentation can be written as inline comments.



WWW.SIGASI.COM/DOWNLOAD

FREE TRIAL download a copy and try Sigasi Studio today

Sigasi;

FEATURES MATRIX	SIGASI STUDIO CREATOR	SIGASI STUDIO XL	SIGASI STUDIO XPRT
	Powerful design entry tool and code browser for all your VHDL or SystemVerilog projects	Our trusted solution for mixed-language projects with powerful extra features	Our next-generation flagship product with powerful visual feedback tools and documentation
Great code editor	✓	✓	✓
Autocomplete	✓	✓	✓
Syntax validation	✓	✓	✓
Format code	✓	✓	✓
Projects	✓	✓	✓
Rename elements	✓	✓	✓
Find references	✓	✓	✓
Basic validation	✓	✓	✓
QuickFixes	✓	✓	✓
Integrates with simulator	✓	✓	✓
Mixed Language (VHDL/SystemVerilog)		✓	✓
Supports UVM		✓	✓
Chip hierarchy		✓	✓
View compilation dependencies		✓	✓
Offline updates		✓	✓
Integrates with linter		✓	✓
Code formatting options		✓	✓
Net search		✓	✓
Extra validation (advanced linting)		✓	✓
Block diagram			✓
State machine diagram			✓
Generate PDF documentation			✓

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